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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Robert H. Havemann                      Docket No.: TI-27506  
Serial No.: 10/001,472                      Examiner: Wojciechowicz, E.  
Filed: 11/01/01                      Art Unit: 2815  
For: INTEGRATED CIRCUIT HAVING A DOPED POROUS DIELECTRIC AND  
METHOD OF MANUFACTURING THE SAME

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<input type="checkbox"/> <b>DIVISIONAL APP'N</b>	
<b>NAME OF INVENTOR(S):</b> Havemann	
<b>TITLE OF INVENTION:</b> Integrated Circuit Having a Doped Porous ...	
<b>FILE NO.:</b> TI-27506	<b>DEPOSIT ACCT. NO.:</b> 20-0868
<b>FAXED:</b> December 13, 2002 <b>DUE:</b> 10/17/2002 <b>ATTY/SECY:</b> JG	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.:</b> 10/001,472 <b>Filing Date:</b> 11/01/2001	

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